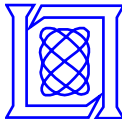
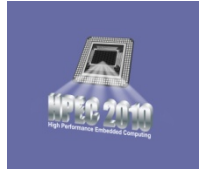


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High Performance Embedded Computing



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2010 HPEC Workshop

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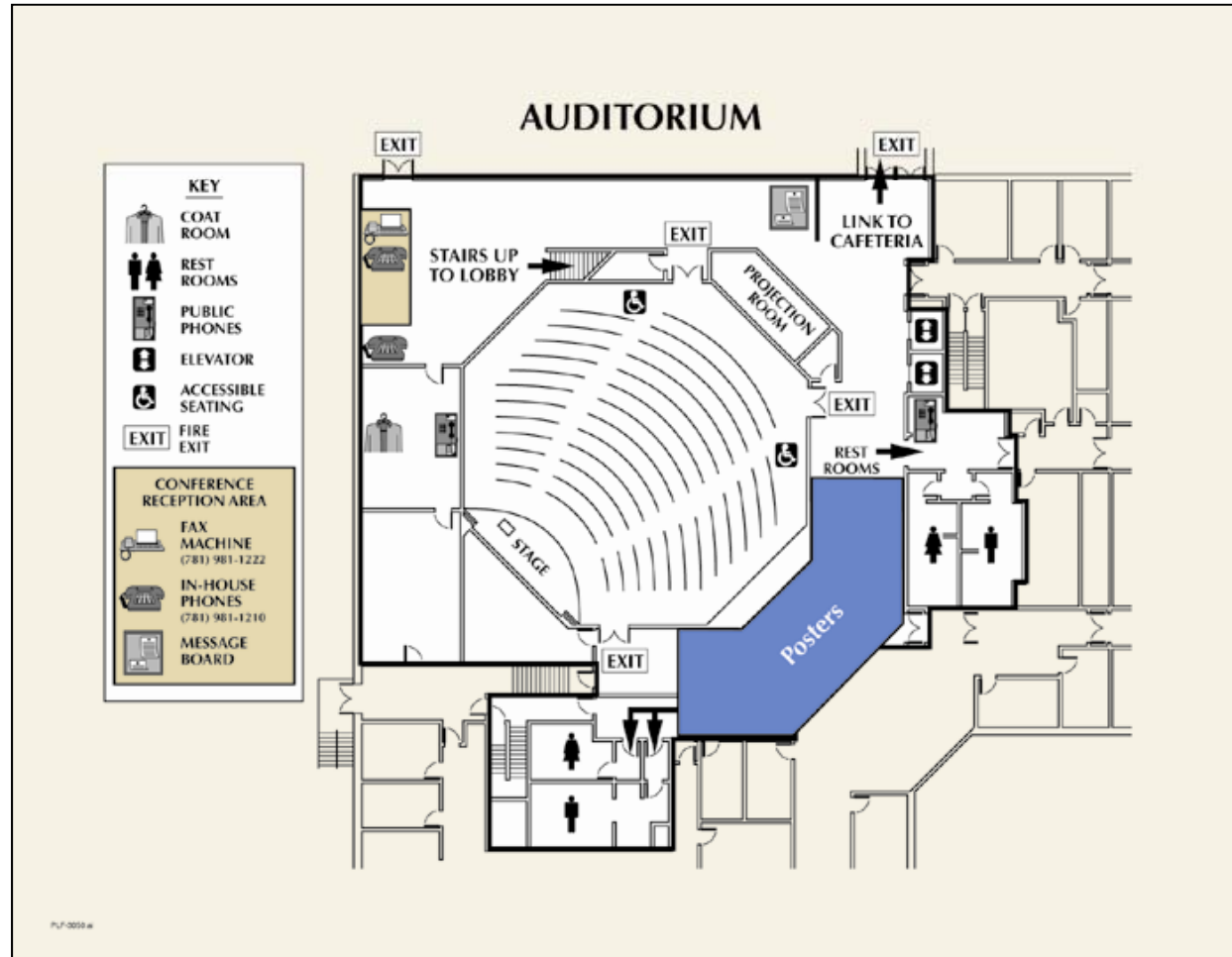


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- **Lunch and refreshments are provided**
- **Wireless devices that include embedded cameras or recording devices are restricted. Please see the Registration Desk for storage**
- **Badges**
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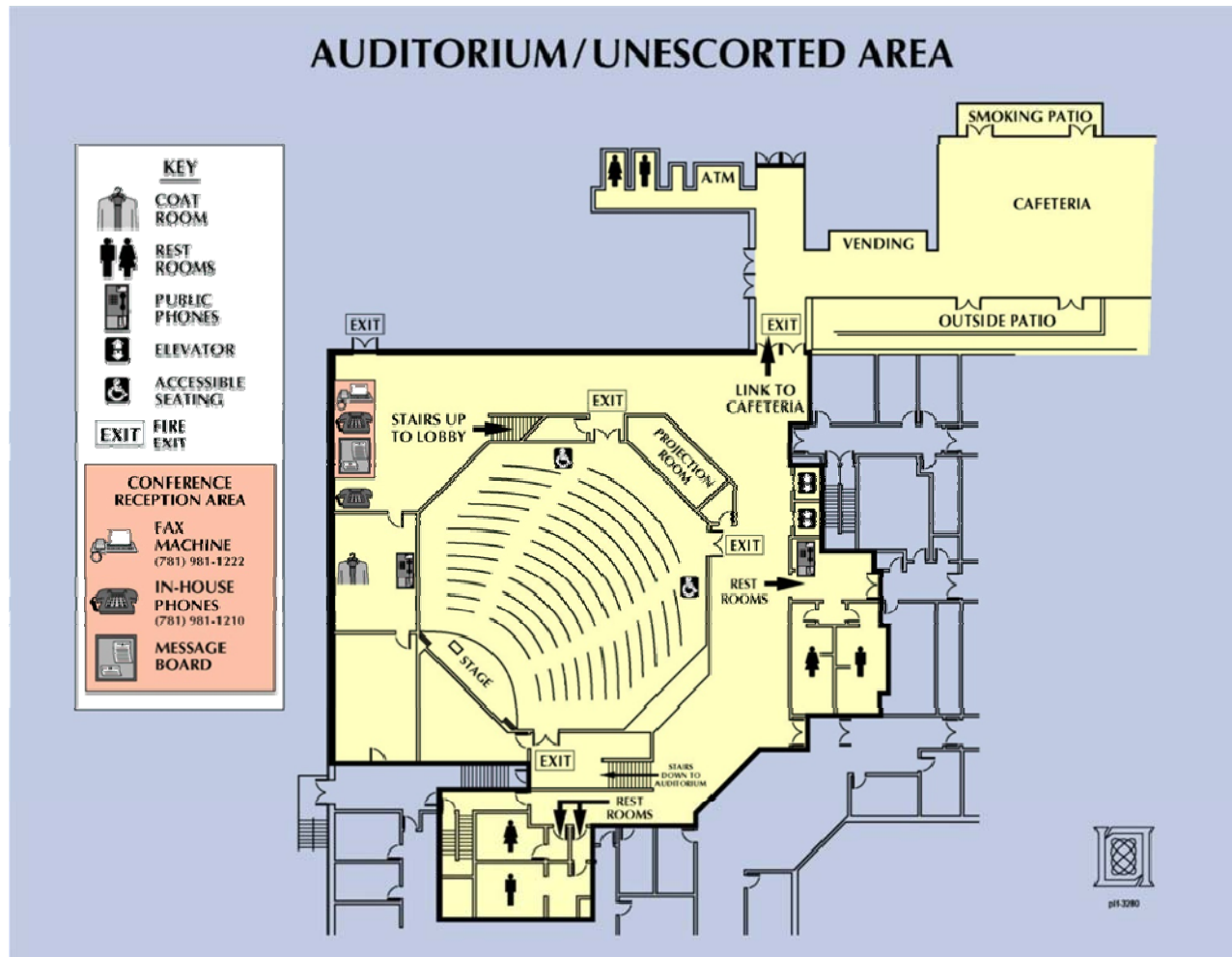
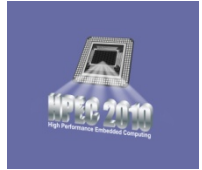


Auditorium

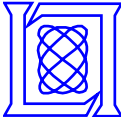




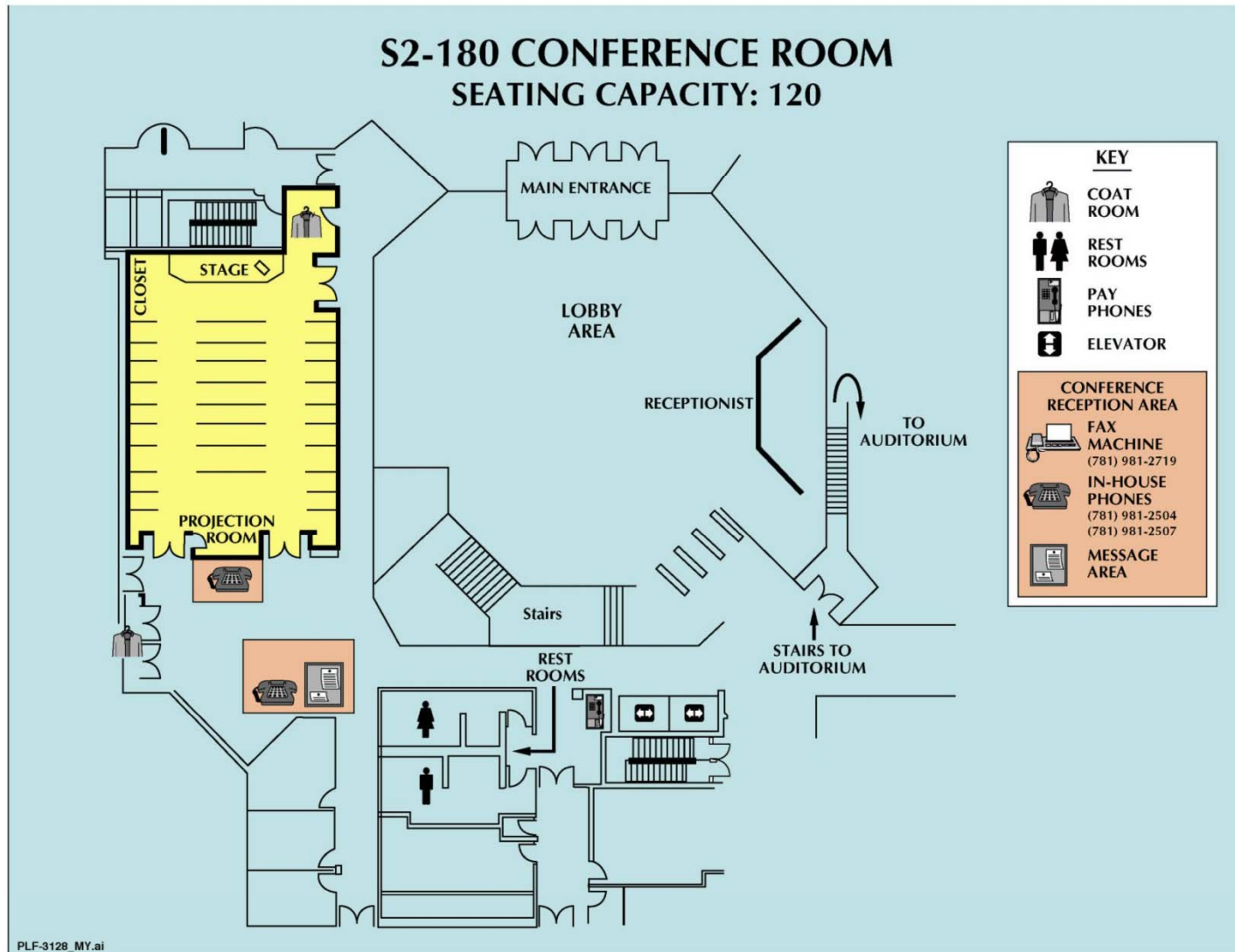
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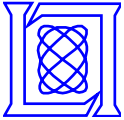


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Focus Sessions in S2-180





Agenda Highlights



Day 1

Wednesday
15 September

Mission Keynote Address – Dr. Peter Lee / DARPA TCTO

Session 1: *GPU*

Focus 1: *Novel Technologies*

Poster / Demo A: *GPU Technologies and Applications*

Session 2: *Many Core Processors*

Focus 2: *U.S. Only & Networked Computing*

Reception and Banquet (Burlington Marriott)

Banquet Presentation – Professor Rahul Sarpeshkar / MIT

Title: Analog and Bio-inspired Computation

Day 2

Thursday
16 September

Technology Keynote Address – Mr. Robert Bond / MIT Lincoln Laboratory

Session 3: *Cloud Computing*

Focus 3: *GPU Frameworks*

Poster / Demo B: *Cloud Technologies and Applications*

Session 4: *Awards Session*

Panel: *ISR Clouds*



Banquet Speaker

Professor Rahul Sarpeshkar

Title: Analog and Bio-inspired Computation

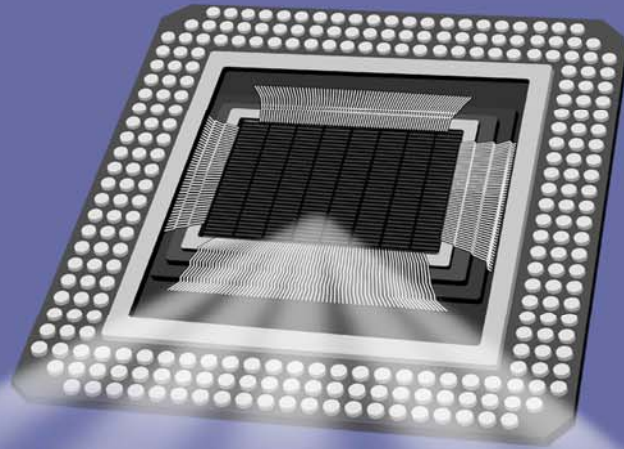
Place: Burlington Marriott

Date: Wednesday, 15 Sept 2010

**Time: 1800 – Reception
1830 – Speaker
1900 – Banquet**



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